

PROGRAMME EDUCATIONAL OUTCOMES - Electronics Engg.

1. To enable student to achieve immediate employment in Electronics, Communication and IT related industries with appropriate title and compensation.
2. To enable student to analyze and solve Electronics Engineering problems by applying basic principles of mathematics, science, and engineering and also able to use modern engineering techniques, skills, and tools to fulfill societal needs.
3. To enable student to innovate, design and develop hardware and software components.
4. To nurture student to be sensitive to ethical, societal and environmental issues while conducting their professional work.
5. To build strong fundamental knowledge amongst student to pursue higher education and continue professional development in Electronics & other fields
6. To equip student with technical and communication skills in order to be able to function in national/international/multi-cultural corporations and organization

Programme Outcomes

1. Ability to communicate effectively with customers and officials, both in writing and orally.
2. An ability to apply knowledge of Electronics, computing, mathematical foundations and theory in the implementation and design of electronics based systems.
3. An ability to interpret data sheets and schematics.
4. An ability to understand and solve real time problems in electronics domain.
5. An ability to develop real time applications based on programming skills.
6. Ability to troubleshoot and maintain electronic and digital circuits.
7. Ability to apply entrepreneurial skills in the awake of incubation centers.
8. Ability to develop, prototype and implement electronic products.
9. An ability to recognize the importance of professional development by pursuing undergraduate studies or face competitive examinations that offer challenging and rewarding career in Electronics.
10. Understanding professional ethical legal security and social issues and responsibilities further to function effectively in a multi disciplinary

SUBJECT OF STUDY AND SCHEME OF EVALUATION SEMESTER I
(Branch: Electronics Engineering)

Semester 1

Sl. No.	Code	Course	Course Category	Periods per week			Credits	Type	Evaluation	
				Theory	Practical	Total			CA	External
1		English for Communication I	F	2	2	4	3	T	50	100
2		Engineering Mathematics I	F	6	0	6	6	T	50	100
3		Engineering Physics I	F	3	0	3	3	T	50	100
4		Engineering Chemistry I	F	3	0	3	3	T	50	100
5		Health and Physical Education	C	1	2	3	2	P	50	50
6		Engineering Graphics	F	2	3	5	0	D	0	0
7		Workshop Practice	F	0	3	3	0	P	0	0
8		Computing Fundamentals	C	2	3	5	4	P	50	50
9		Engineering Science Lab	F		3	3	0	P	0	0
Total				19	16	35	21			

Abbreviations used:

Course Categories F – Foundation Courses, C – Common Courses, B – Basic Technology Courses, A – Applied Technology Courses, E – Elective Courses.
Evaluation Type – T – Theory, P – Practical, D – Drawing, Pr – Project.

SUBJECT OF STUDY AND SCHEME OF EVALUATION SEMESTER II

(Branch: Electronics Engineering)

Semester 2

Sl. No.	Code	Course	Course Category	Periods per week			Credits	Type	Evaluation	
				Theory	Practical	Total			CA	External
1		English for Communication II	F	2	2	4	3	T	50	100
2		Engineering Mathematics II	F	6	0	6	6	T	50	100
3		Engineering Physics II	F	3	0	3	3	T	50	100
4		Engineering Chemistry II	F	3	0	3	3	T	50	100
5		Basic Electronics	B	4	0	4	4	T	50	100
6		Engineering Graphics	F	1	3	4	5	D	50	100
7		Workshop Practice	F	0	3	3	3	p	50	50
8		Engineering Science Lab	F	0	3	3	3	P	50	50
9		Basic Electronics Lab	B	0	3	3	2	P	50	50
10		Life Skill	C	1	1	2	2	P	50	50
Total				20	15	35	34			

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**SUBJECT OF STUDY AND SCHEME OF EVALUATION SEMESTER III
(Branch: Electronics Engineering)**

Semester 3

Sl. No.	Code	Course	Course Category	Periods per week			Credits	Type	Evaluation	
				Theory	Practical	Total			CA	External
1		Electrical Technology	B	4	0	4	4	T	50	100
2		Electronic Devices and Circuits	B	5	0	5	5	T	50	100
3		Digital Electronics	B	4	0	4	4	T	50	100
4		Communication Engineering	B	4	0	4	4	T	50	100
5		Environmental Science and Disaster Management	C	3	0	3	3	T	50	100
6		Electronics Circuits Lab	B	0	5	5	3	P	50	50
7		Digital Electronics Lab	B	0	5	5	3	P	50	50
8		Analogue Communication Lab	B	0	5	5	3	P	50	50
Total				20	15	35	29			

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Evaluation Type – T – Theory, P – Practical, D – Drawing, Pr – Project.

**SUBJECT OF STUDY AND SCHEME OF EVALUATION SEMESTER IV
(Branch: Electronics Engineering)**

Semester 4

Sl. No.	Code	Course	Course Category	Periods per week			Credits	Type	Evaluation	
				Theory	Practical	Total			CA	External
1		Linear Integrated Circuits	B	4	0	4	4	T	50	100
2		Electronics Instruments and Measurements	A	4	0	4	4	T	50	100
3		Microcontroller and Interfacing	A	5	0	5	5	T	50	100
4		Programming in C	B	4	0	4	4	T	50	100
5		Linear Integrated Circuits Lab	A	0	6	6	3	P	50	50
6		Microcontroller and Interfacing Lab	A	0	6	6	3	P	50	50
7		Programming in C Lab	B		6	6	3	P	50	50
8		Mini Project	A	(5 days)			5	P	50	50
Total				17	18	35	31			

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SUBJECT OF STUDY AND SCHEME OF EVALUATION SEMESTER V
(Branch: Electronics Engineering)

Semester 5

Sl. No.	Code	Course	Course Category	Periods per week			Credits	Type	Evaluation	
				Theory	Practical	Total			CA	External
1		Industrial Management and Safety	C	4		4	4	T	50	100
2		Embedded System	A	4		4	4	T	50	100
3		Industrial Electronics and PLC	A	4		4	4	T	50	100
4		Control System	E	4		4	4	T	50	100
		Medical Electronics	E							
		Optical Fibre Communication	E							
			E							
5		Embedded System Lab	A		4	4	2	P	50	50
6		Industrial Electronics and PLC Lab	A		4	4	2	P	50	50
7		PCB and SPICE Lab	A		5	5	3		50	50
8		Industrial visit	P	2 Weeks			2	P	50	0
9		Project and Seminar	P		6	6		Pr	0	0
Total				16	19	35	25			

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SJECT OF STUDY AND SCHEME OF EVALUATION SEMESTER VI
(Branch: Electronics Engineering)

Semester 6

Sl. No.	Code	Course	Course category	Periods per week			Credits	Type	Evaluation	
				Theory	Practical	Total			CA	External
1		Communication Systems	A	5		5	5	T	50	100
2		Computer Hardware and Networking	A	5		5	5	T	50	100
3		Advanced Microprocessors	A	5		5	5	T	50	100
4		Radar and Navigation	E	4		4	4	T	50	100
		Television Engineering	E							
		Digital Signal Processing	E							
			E							
5		Computer Hardware and Networking Lab	A		5	5	3	P	50	50
6		HDL and Simulation Software Lab	A		5	5	3	P	50	50
7		Project and Seminar	P		6	6	10	Pr	10+40	10+40
8										
Total				19	16	35	35			

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Evaluation Type – T – Theory, P – Practical, D – Drawing, Pr – Project.